



Attorney Docket No. 0553-0152.02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Shunpei YAMAZAKI et al

Serial No.: 10/684,936

Filed: October 14, 2003

Art Unit:

For: SEMICONDUCTOR DEVICE AND METHOD
OF FABRICATING THE SAME

I hereby certify that this correspondence
is being deposited with the United States
Postal Service as first class mail in an
envelope addressed to:
Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450, on January 15,
2004

Cristine M. Hall

Date: January 15, 2004

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. A copy of each of the references was submitted to the Patent Office or was cited by the Examiner in prior application serial no. 10/093,313 filed March 7, 2002, (now US patent no. 6,645,826, issued November 11, 2003) which is a divisional of the parent application serial no. 09/472,137 filed December 23, 1999, (now US patent no. 6,380,558, issued April 30, 2002) which is being relied upon for an earlier filing date under 35 U.S.C. 120 (37 C.F.R. §1.98(d)).

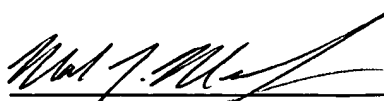
No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

A first office action, notice of allowance or issue fee notification has not been received in this case, so applicant does not believe that a fee is due. However, if any such fee is required, please charge our Deposit Account No. 50/1039.

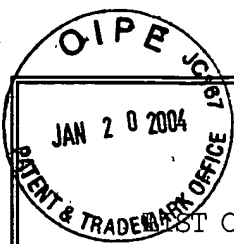
Respectfully submitted,

A handwritten signature in dark ink, appearing to read 'Mark J. Murphy', is written over a horizontal line.

Mark J. Murphy

Registration No: 34,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, LTD.
200 West Adams Street
Suite 2850
Chicago, Illinois 60606
(312) 236-8500



LIST OF PUBLICATIONS CITED BY APPLICANT	<u>Atty. Docket No.</u> 0553-0152.02	<u>Serial No.</u> 10/684,936
	<u>Applicant</u> Shunpei YAMAZAKI et al	
	<u>Filing Date</u> October 14, 2003	<u>Group</u>

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	5,247,190	09/21/93	Friend et al			04/18/90
	5,399,502	03/21/95	Friend et al			05/05/93
	5,594,569	01/14/97	Konuma et al			07/20/94
	5,643,826	07/01/97	Ohtani et al			10/25/94
	5,707,746	01/1998	Yaoi et al			
	5,747,830	05/1998	Okita			
	5,771,110	06/1998	Hirano et al			
	5,923,962	07/13/99	Ohtani et al			04/28/95
	6,271,897	08/2001	Ichikawa et al			
	6,274,516	08/2001	Kamei et al			
	6,420,282	07/2002	Batey et al			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	APPLICANT	English Abstract	English Trans.	FILING DATE
	WO 90/13148	11/01/90	Cambridge Research and Innovation Ltd.			04/18/90
	JP 7-130652	05/19/95	Semiconductor Energy Lab Co. Ltd.	X		10/29/93
	JP 7-335900	12/22/95	Semiconductor Energy Lab Co. Ltd.	X		06/09/94
	JP 8-078329	03/22/96	Semiconductor Energy Lab Co. Ltd.	X		09/05/94
	JP 10-092576	04/10/98	Cambridge Display Technology Ltd.	X		04/18/97
	JP 10-135468	05/22/98	Semiconductor Energy Lab Co. Ltd.	X		10/24/96
	JP 10-135469	05/22/98	Semiconductor Energy Lab Co. Ltd.	X		10/24/96
	JP 10-247735	09/14/98	Semiconductor Energy Lab Co. Ltd.	X		03/03/97



OTHER PRIOR ART - NON-PATENT LITERATURE DOCUMENTS
(Including Author, Title, Date, Pertinent Pages)

- 1) YEH, J.L. et al, "Structural and Optical Properties of Amorphous Silicon Oxynitride," J. Appl. Phys., vol. 79, no. 2, pp. 656-663, January 15, (1996).
- 2) INUI, S. et al, "Thresholdless Antiferroelectricity in Liquid Crystals and its Application to Displays," J. Mater Chem., vol. 6, no. 4, pp. 671-673, (1996).
- 3) YOSHIDA, T. et al, "A Full-Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," SID 97 Digest, pp. 841-844, (1997).
- 4) FURUE, H. et al, "Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCD Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," SID 98 Digest, pp. 782-785, (1998).
- 5) TERADA et al, "Half-V Switching Mode FLCD," Proceedings of the 46th Applied Physics Association Lectures, 28p-V-8, p. 1316, March, (1999).
- 6) SCHENK, H. et al, "Polymers for Light Emitting Diodes," **EURODISPLAY '99**, Proceedings of the 19th International Display Research Conference, Berlin, Germany, September 6-9, pp. 33-37 (1999).
- 7) YOSHIHARA, T. et al, "Time Division Full Color LCD by Ferroelectric Liquid Crystal," **EKISHO**, Vol 3, no. 3, pp. 190-194, (1999).

EXAMINER:

DATE CONSIDERED:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.



LIST OF PUBLICATIONS CITED BY APPLICANT	<u>Atty. Docket No.</u> 0553-0152.02	<u>Serial No.</u> 10/684,936
	<u>Applicant</u> Shunpei YAMAZAKI et al	
	<u>Filing Date</u> October 14, 2003	<u>Group</u>

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	5,306,651	04/26/94	Masumo et al	437	40	05/10/91
	5,640,067	06/17/97	Yamauchi et al	313	504	03/18/96
	5,731,613	03/24/98	Yamazaki et al	257	350	08/09/95
	5,815,223	09/29/98	Watanabe et al	349	42	09/30/97
	5,897,328	04/27/99	Yamauchi et al	438	29	05/13/97
	6,048,758	04/11/00	Yamazaki et al	438	166	01/23/97
	US 2001/ 0004121 A1	06/21/01	Sakama et al	257	347	12/19/00

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	APPLICANT	English Abstract	English Trans.	FILING DATE
	JP 56-111258	09/02/81	Chou-L.S.I. Gijutsu Kenkyu Kumiai		X	01/07/80
	JP 58-164268	09/29/83	K.K. Suwa-Seikosha		X	03/25/82
	JP 64-35961	02/07/89	Richo Co., Ltd.		X	07/30/87
	JP 1-276672	11/07/89	Seiko Corp.		X	04/27/88
	JP 5-55581	03/05/93	Semiconductor Energy Lab Co. Ltd.		X	08/26/91

OTHER PRIOR ART - NON-PATENT LITERATURE DOCUMENTS
(Including Author, Title, Date, Pertinent Pages)

--	--

EXAMINER:	DATE CONSIDERED:
-----------	------------------

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.